

**Search Notes**

Application/Control No.

09/836,480

Examiner

Ted M. Wang

Applicant(s)/Patent under  
Reexamination

MIYANO, KAZUTAKA

Art Unit

2634

**SEARCHED**

Class	Subclass	Date	Examiner
375	373	9/22/2005	TW
375	327	9/22/2005	TW
327	102	9/22/2005	TW
327	147	9/22/2005	TW
327	159	9/22/2005	TW
331	1R	9/22/2005	TW

**INTERFERENCE SEARCHED**

Class	Subclass	Date	Examiner
375	376,375	9/22/2005	TW
375	373,327	9/22/2005	TW
327	102,147	9/22/2005	TW
327/159 331/1R		9/22/2005	TW

**SEARCH NOTES  
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
EAST - USPGPUB, USPAT, USOCR, EPO, JPO, DERWENT, IBM_TDB, see attached search report	9/22/2005	TW
IEEE - Advance search - (DLL or delayed locked loop or PLL or phase locked loop) and constant current	9/22/2005	TW
IEEE - Advance search - duty correction and phase lock	9/22/2005	TW
IEEE - Advance search - frequency to voltage converter and constant current and differential amplifier	9/22/2005	TW
GOOGLE - (DLL or delayed locked loop or PLL or phase locked loop) and constant current and duty correction and phase lock	9/22/2005	TW
GOOGLE - (DLL or PLL) and frequency to voltage converter and constant current and differential amplifier	9/22/2005	TW